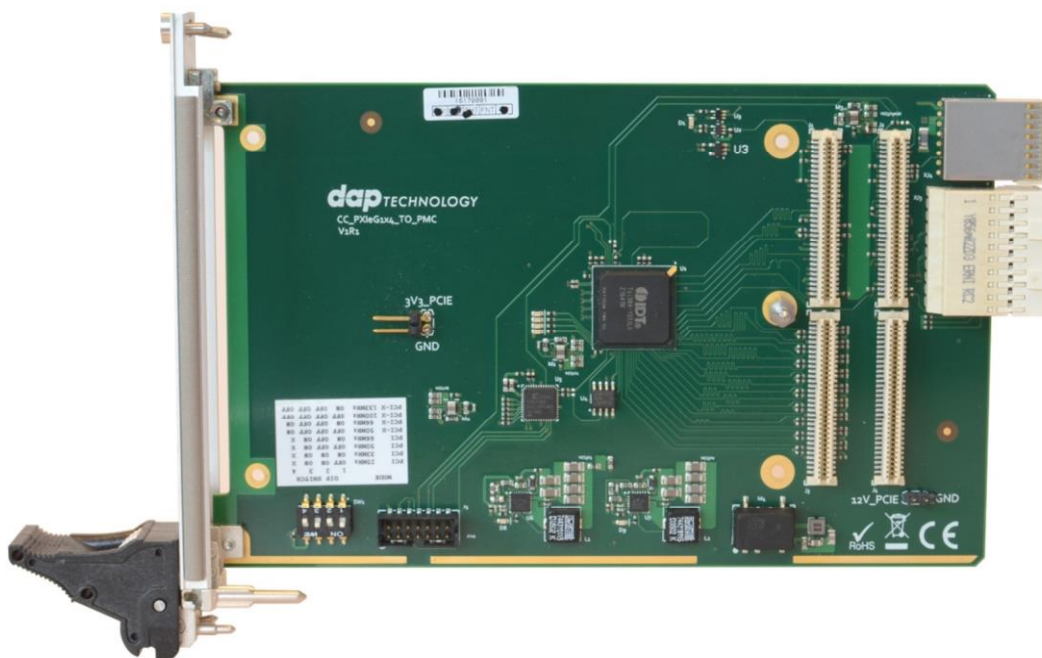


Data Sheet Carrier Card PXI Express to PMC



Use PMC cards in a PXI Express / cPCIe chassis
Easy installation



Carrier Card PXI Express to PMC

PCI Express Interface

| | |
|-----------------------------|--|
| number of PCI Express lanes | 4 |
| maximum speed per lane | 2.5Gbps |
| Form Factor | PXI Express Peripheral Module / cPCIe Type 2 Peripheral Board |

Bridge

| | |
|------------------|--|
| bridge chip type | Tundra Tsi384 |
| compliance | PCI Express PCI/PCI-X Bridge 1.0 PCI-to-PCI Bridge Architecture 1.2 |

PMC Interface

| | |
|-----------------|---|
| PCI width | 32 or 64 bits |
| PCI speed | 25, 33, 50 or 66 MHz |
| PCI-X width | 32 or 64 bits |
| PCI-X speed | 50, 66, 100 or 133 MHz |
| width selection | automatic |
| speed selection | semi-automatic (switch settings limit bus speed) |
| IO Voltage | 3.3V |
| compliance | PCI Local Bus 3.0 PCI-X 2.0 (mode 1 only) PCI Bus PMI 1.2 |



Carrier Card PXI Express to PMC

Power System

| | |
|-----------------------|------------------------------|
| 12V to PMC source | 12V, 24W PXI Express 12V |
| -12V to PMC source | -12V, 2W PXI Express 12V |
| 5V to PMC source | 5V, 25W PXI Express 12V |
| 3V3 to PMC source | 3V3 , 15W PXI Express 12V |
| PMC card power | 25W |

Other Specifications

| | |
|-------------------|--------------------|
| power consumption | 1.5W |
| compliance | CE RoHS WEEE |



Carrier Card PXI Express to PMC

Mode Settings

| MODE | DIP SWITCH | | | |
|-----------|------------|-----|-----|-----|
| | 1 | 2 | 3 | 4 |
| PCI 25 | off | on | on | x |
| PCI 33 | on | on | on | x |
| PCI 50 | off | off | on | x |
| PCI 66 | on | off | on | x |
| PCI-X 50 | off | off | off | on |
| PCI-X 66 | on | off | off | on |
| PCI-X 100 | off | off | off | off |
| PCI-X 133 | on | off | off | off |

Slot Identification

Software can identify the physical location of the carrier card in the PXI chassis by means of the EEPROM Control Register (please refer to the Tsi384 User Manual for more details).

Reading EEPROM address 0xFFFF will return a number ranging from 0 to 32 representing the physical slot number.

Reading a byte of EEPROM data consists of a write and a read to/from the EE_CTRL register. In pseudo code:

```
write_pci_reg(carrier_id, 0x0AC, 0x19FFFF00) pxi_slot  
= read_pci_reg(carrier_id, 0x0AC) & 0xFF
```



Ordering Information

Part number: CC_PXleG1x4_TO_PMC