



GEN2 TRIPLE SERIES FIRESPY 3470bT





PRODUCT OVERVIEW:

The **FireSpy3470bT** bus analyzer is an interesting addition to the Triple analyzer series from DapTechnology. Based on the proven architecture of the **FireSpy3850** this new product meets many customer requests for this 3U CPCI/PXI form factor! Just like the **FireSpy3850** it combines three FireSpy analyzers in one single instrument. It comprises a significantly more powerful on-board processor and improved connectivity to the host.

The Compact PCI form factor makes this product very appealing as the solution enables users to install the FireSpy hardware in any 3U CPCI or PXI card slot.

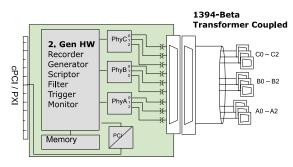
The FireSpy3470bT has three 1394 nodes connected to three synchronized analyzer engines. They are controlled by an on-board RISC processor running at 400 MHz. Each node is connected to three active transformer coupled ports. All nine ports are connected via a high-speed high density connector on the front bezel and are made accessible via a special adapter cable (with 1394b Beta connectors).

The **FireSpy3470bT** is equipped with 1 GByte memory and extensive hardware filtering and trigger capabilities. On the host you can control the FireSpy using a graphical user interface to analyze and display the bus traffic in a user-friendly way, or you can use the API and program your own controlling software.

Key Features include:

- IEEE 1394-1995, 1394a-2000 and 1394b-2008
- 200b, 400b transfer rates
- PICMG 2.0 R3.0 and PXI 2.2 compliant
- On-board 400 MHz RISC processor and programmable logic
- 1024 MByte internal memory
- GUI and API for Windows[™] Operating Systems
- 3 analyzing nodes with a total of 9 1394 ports
- All ports are active transformer coupled
- Powerful software provides:
 - Monitor
 - o Recorder
 - o Commander
 - Scriptor
 - Generator
 - o Filter and Trigger
 - Support for AS5643, IEC61883, AV/C, SBP2, IP1394 and IIDC protocols
- Internal SelfTest
- C/C++ API with wrappers for LabVIEW[™]

FS3470bT



A COMPLETE SOLUTION:

The **FireDiagnostics Suite** is the most comprehensive collection of 1394 analysis, simulation and interface tools for a wide range of applications. Apart from well established and hardware assisted analyzer tools like Monitor, Recorder, Generator, Commander and Scriptor, the suite also offers a set of software tools designed to integrate the FireSpy products in a wide variety of testing applications, as well as extend customization of its functionality beyond the baseline feature set provided by DapTechnology.

The foundation for all software tools included in the FireDiagnostics Suite is formed by the **Application Programming Interface** (API). With its interfaces for a wide range of development environments like C/C++ and support for the Windows operating system, the application of FireSpy analyzers is extremely flexible. With its feature-rich function library, all hardware assisted analyzer tools like the Recorder and Generator can be controlled as well as more lowlevel 1394 bus functions.

The **Recording Viewer** is a standalone application designed to permit trace (recorded data) analysis offline, i.e. without a connected FireSpy. The same comprehensive set of analysis tasks is available but allows for a much smaller PC footprint than having the entire FireSpy application installed.

The **Signal Monitor** is an easy-to-use Mil1394 sub-system monitor and analysis tool that benefits from the hardware-implemented Mil1394 protocol. A customizable set of status signals can be pulled from the bus and displayed in near real-time on a customizable graphical Control Panel. Alarms can be setup to alert the operator of out-of-range values.

Another cornerstone of the FireSpy products is the unparalleled high-level **protocol support**. Besides the hardware-assisted integration of AS5643 the FireSpys also support software-based analysis capabilities for consumer and industrial control based applications. The different protocols require very different implementation details and are therefore very unique in their implementation. However, some key characteristics can be identified and are listed below:

- Nested protocol header decoding
- Protocol payload separation
- Handshake verification
- Logical grouping of related transactions
- Separate protocol view
- Protocol layer CRC and Parity Check
- · Customization of display details

Additionally, separate applications (**Format Editor and Protocol Editor**) allow for the modification and extension of the factory default decoding and identification definitions. This extremely powerful and versatile tool enables experienced users to build on top of the standard definitions, engage in early prototyping and benchmarking of protocols still in the specification development process, as well as add proprietary extensions.



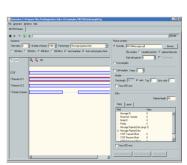
The *Recorder* is the main tool for data traffic capturing and analysis. Running all in HW/FW it guarantees precise time measurement, reliable data capture, instantaneous triggering and enough memory for even very complex analysis tasks. It contains several display views, which can all be switched on or off individually.

Time View - timing analysis of events and packets, resolution of 10 ns. **Packet View** - chronological packet display with Trigger indicator and error verification

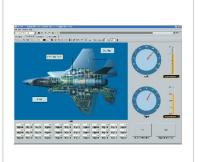
Transaction View - transaction-oriented display, verification of transaction completeness, transaction list or flow-diagram display

Topology View - static bus-topology display at the trace cursor position **Protocol View** - high-level protocol analysis, encapsulated protocol verification, handshake verification, etc. The **Generator** is optimized for the generation of isochronous stream data packets offering the most comprehensible feature set for the insertion of errors, streaming of simultaneous channels and payload definitions from stored files.

The **Stream Generator** includes a powerful graphical editor to specify slots with stream sequences to be sent



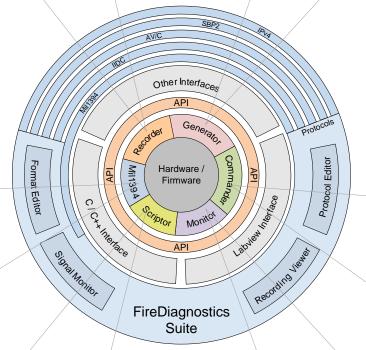
for up to 63 channels. Each sequence consists of one or more stream packets with selectable data sources that can be fixed or from file. For each sequence one can select various options such as speed, packet size and header fields, including erroneous values. The overall sequence size is customizable in multiples of Cycle Periods. All Generator slots can be run in a looped-mode continuous transmission. Both the **Stream Generator** and the **Scriptor** can run in parallel for advanced isochronous and asynchronous combination testing.

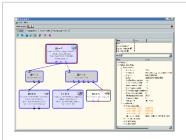


The **AS5643** protocol HW level support for the FireSpy is an essential component for supporting AS5643. Key features include:

Generation - STOF and stream generation, 1µs resolution Verification / Calculation - Timing, Vertical Parity Check, Heartbeat Monitoring - asynchronous stream payload field extraction







The *Commander* can be used to control the FireSpy functionality on a basic 1394 protocol level:

Topology - live display of the current bus topology, Configuration ROM Explorer

Memory Read/Write - R/W/L to memory locations of remote nodes, Packet S/R - RX/TX of all packets, unformatted and erroneous packets.

PHY Register - R/W of PHY registers of the local and R of the remote nodes.



The **Scriptor** permits the definition of C-like scripts to control almost anything on the FireSpy, including sending and receiving packets. It is the preferred tool for the generation of individual asynchronous packets, asynchronous sequences and the simulation of entire handshakes.

Script editor - C-like script editor/compiler with automatic code block generation, integrated Debugger and floating-point data type support **Data editor** - defines data elements that can be used by the script, i.e. generation data

Control Panel - display of values using different types of meters (gauge, LCD, thermometer, etc.).

The *Monitor* gives a quick indication of activities on the bus under test. The displayed data is updated in real time.

- Number of packets of specific types
- Number of packets of specific speeds
- Number of acknowledge packets
- Number of error packets
- Total number of packets
- Number of bus resets
- · Bus voltage measurement



MAIN FEATURE SUMMARY:

GENERAL

- IEEE 1394-1995, 1394a-2000 and 1394b-2002 compliant
- Supports 200(B) and 400(B) Mbps transfer rates
- CompactPCI compliant
- 992 MByte memory for packet and data storage
- Firmware field upgradeable to enable future expansions
- AUX connector for:
 - $\circ~$ Trigger input and output functions
 - Recording external event
- GUI and API for Windows[™] Operating Systems

MONITOR

- Displays bus activity:
 - o isochronous packets
 - o all types of asynchronous packets
 - o all types of PHY packets
 - o all types of acknowledge packets
- several types of ErrorsCounts packets according to type, speed, ack and error condition
- Counts packets according to
 Counts number of bus resets
- Measurement of bus power voltages (bilingual ports)

RECORDER

- Time stamping of all packets and status events with 10ns resolution
- Packets hidden by slower connections are visible as 'prefix only' packets
- Extensive packet/event filtering/trigger/search capabilities
 - Packet type
 - o Transmission speed
 - Boolean combination of 4 programmable packet sets
 - $_{\odot}~$ Data payload patterns
 - Error conditions
 - Various status events
 - o Graphical Trigger Sequencer
- · Adjustable trigger position within programmable record buffer size
- Cyclic pre-trigger buffer management option
- Different kinds of packet display views, including:
 - o Time View, displays all packets on a time line, including the prefix
 - o Packet View, displays packets as list plus selected packet options
 - Transaction View, displays transactions as list or flow graph
 - o Topology View, graphical topology displays as is during recording
 - Protocol View, displays packets decoded to selected protocol
- Precise time measurements
- Marking of individual packets or packet ranges
- · Export format for re-generation of packets by Scriptor or API

GENERATOR

- Simultaneous generation of up to 63 iso streams
 - o Graphically programming of stream transmit block
 - o Data payload import from file
- Generator and Scriptor run simultaneous for stream and asynchronous packet generation
- Special Mil1394 stream generator package (optional)

SCRIPTOR

- Script Editor
 - o C-like scripting language
 - Function Library
 - o Macros to automatically generate blocks of code
 - Syntax coloring
 - Integrated Debugger
 - Floating point data types
- Data Editor
- Control Panel
 - Graphical display elements for data value representation
 - Ethernet-connected Client Panels for remote data monitoring
- Several Sample Scripts

COMMANDER

- Reading and/or writing of local and reading of remote PHY registers
- Reading and/or writing of remote memory locations (incl. CSR register space)
- Possibility to graphically view the current Topologies
- Sending of user definable packets

SPECIFICATION:

Dimensions: Weight: Operating Range: Power Requirements: Compliance: Connections:

Indicators: Switches:

Package Content:

Product warranty: Part Number:

Optional Configuration:

SW Add-on modules:

3U CPCI card, 213x130x20 mm 250 g 0 – 70 C 10 Watt maximum FCC Class A rear: PXI J1, J2 front: 68p SCSI II/III connector carrying 9 active transformer coupled IEEE1394 Beta

FireSpy3470bT Adapter Cable (SCSI – 1394Beta (x9))

36 months limited warranty

FS347bT or FS347bTAS5643 w. AS5643 SW protocol package

SBP2 protocol software package IIDC protocol software package AV/C protocol software package IP1394 protocol software package AMI-C protocol software package AS5643 protocol software package



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